J-Link WiFi

Overview

J-Link WiFi is the wireless option, when the targeted embedded device is moving or has to be shielded from the environment. It is USB powered, so that it can use the same power source as the target device. It can communicate at high speed with the supported target CPUs.

The J-Link debug probes are supported by all major IDEs including Eclipse, GDB-based IDEs and SEGGER Embedded Studio. For a complete list, please refer to Supported IDEs. J-Link is probably the most popular debug probe for ARM and RISC-V cores, making it the de-facto standard.

Specification

- Host interface: WiFi
- Power supply: USB
- Download speed to RAM: 1.0MB/s
- Max target inteface speed: 15MHz
- Max SPI interface speed: 12MHz
- Max SWO speed: 30MHz
- High speed sampling bandwith: 1kHz
- Supported target voltage: 1.2-5V

Key features

- Provides J-Link debugging for mobile applications
- Built-in VCOM functionality
- Comes with integrated licenses for: Unlimited breakpoints in flash memory (Unlimited Flash Breakpoints), RDI / RDDI and J-Flash
- Supports a broad range of microcontrollers
- Multiple CPUs supported—8051, PIC32, RX, ARM7/9/11, Cortex-M/R/A, RISC-V
- Supports direct download into RAM and flash memory

Package content

- J-Link WiFi
- USB cable
- 20-Pin Target cable

Order number

8.14.28